

# SANYO Semiconductors DATA SHEET

# LA1150 — FM IF Amplifier and Detector IC

#### **Overview**

The LA1150 is an FM IF amplifier and detector IC.

#### Features

- FM IF amplifier and limiter.
- Demodulated output : 600mVrms (maximum).
- Total harmonic distortion : 0.3% (maximum).

#### **Specifications**

**Maximum Ratings** at  $Ta = 25^{\circ}C$ 

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		16	V
Maximum input voltage	VIN		0.7	Vrms
Allowable power dissipation	Pd max	Derate by 4mW per 1°C when used at temperatures above 25°C.	400	mW
Operating temperature	Topr		-20 to +80	°C
Storage temperature	Tstg		-40 to +125	°C

#### **Recommended Operating Conditions** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V <sub>CC</sub>		12	V

#### Operating Characteristics at Ta = 25°C, V<sub>CC</sub> = 12V, f = 10.7MHz, fm = 400Hz, 100% modulation

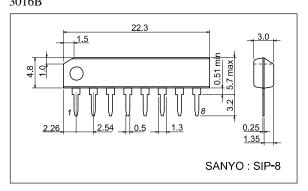
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Current drain	ICC		8	12	15	mA
Output pin (pin 7) DC voltage	V <sub>7</sub>		3.7		5.2	V
Demodulated output	VO	$V_{IN} = 80 dB\mu$	400		600	mVrms
Input limiting voltage	V <sub>IN</sub> (lim)	-3dB limiting		50	55	dBμ
Total harmonic distortion	THD	$V_{IN} = 80 dB\mu$		0.2	0.3	%
Signal-to-noise ratio	S/N	$V_{IN} = 80 dB\mu$	70	80		dB
AM rejection ratio	AMR		47	53		dB
Output resistance	Ro	f = 400Hz	6.2	7.7	9.5	kΩ

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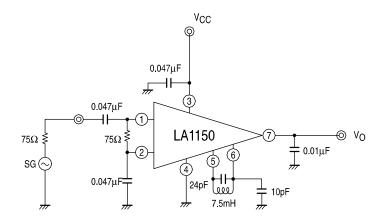
SANYO Electric Co., Ltd. Semiconductor Company TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

# **Package Dimensions**

unit : mm 3016B

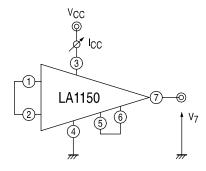


# **AC Test Circuit**



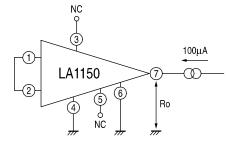
PCA00608

### **DC Test Circuit**



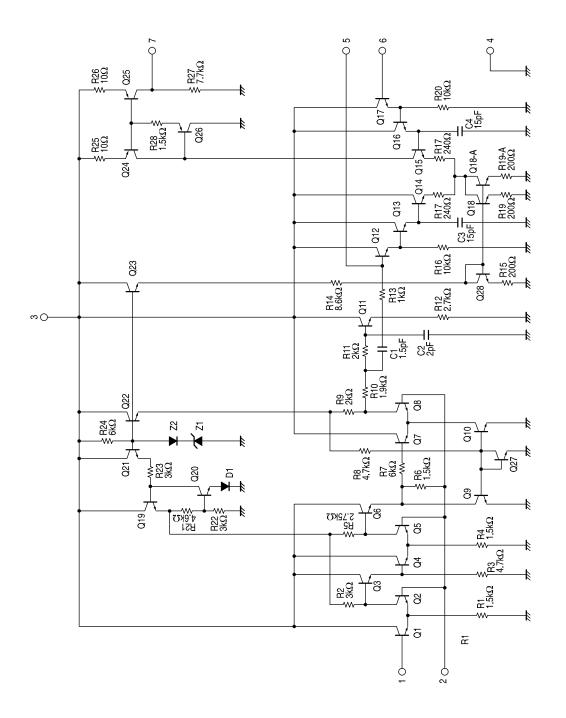
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## **Ro Test Circuit**



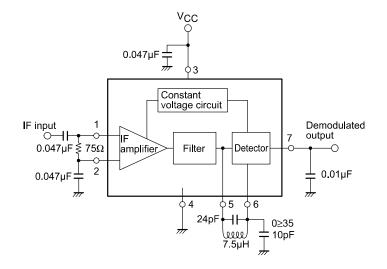
PCA00610

**Equivalent Circuit** 



PCA00611

#### **Application Circuit Example**



PCA00612

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