TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSVII)

# **TK20J50D**

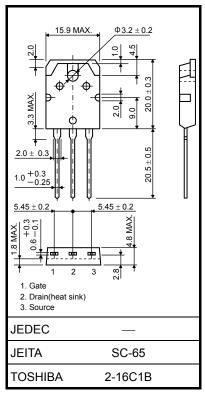
### **Switching Regulator Applications**

Unit: mm

- Low drain-source ON-resistance: RDS (ON) =  $0.23 \Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 8.5 \text{ S (typ.)}$
- Low leakage current:  $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 500 \text{ V)}$
- Enhancement mode:  $V_{th} = 2.0 \text{ to } 4.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$

### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	500	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	20	Α	
	Pulse (t = 1 ms) (Note 1)	I <sub>DP</sub>	80		
Drain power dissipati	on (Tc = 25°C)	PD	280	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	470	mJ	
Avalanche current		I <sub>AR</sub>	20	Α	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	28	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

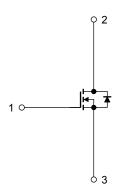
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	0.446	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 90 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial), L = 2.0 mH,  $R_G = 25 \Omega$ ,  $I_{AR} = 20 \text{ A}$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



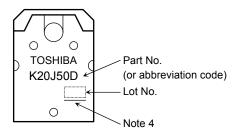
# Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μΑ
Drain cut-off current		I <sub>DSS</sub>	V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0 V	_	_	10	μА
Drain-source breakdown voltage		V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	500	_	_	V
Gate threshold ve	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	٧
Drain-source ON	-resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A	_	0.22	0.27	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 10 A	2.4	8.5	_	S
Input capacitance		C <sub>iss</sub>		_	2600	_	pF
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	11	_	
Output capacitance		C <sub>oss</sub>		_	280	_	
Switching time	Rise time	t <sub>r</sub>	$\begin{array}{c c} 10 \text{ V} & \text{I}_D = 10 \text{ A} & \text{V}_{OUT} \\ \hline \text{VGS} & \text{V} & \text{RL} = \\ 50 \ \Omega & \text{V}_{DD} \approx 200 \text{ V} \\ \\ \hline \text{Duty} \leq 1\%, \ t_W = 10 \ \mu\text{s} \end{array}$	_	50	_	
	Turn-on time	t <sub>on</sub>		_	100	_	no
	Fall time	t <sub>f</sub>			25		ns
	Turn-off time	t <sub>off</sub>		_	150	_	
Total gate charge		Qg		_	45	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$	_	28	_	nC
Gate-drain charge		Q <sub>gd</sub>		_	17	_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

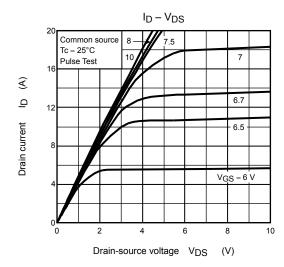
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	20	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	80	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 20 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 20 A, V <sub>GS</sub> = 0 V,	_	1700	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> /dt = 100 A/μs		26	_	μС

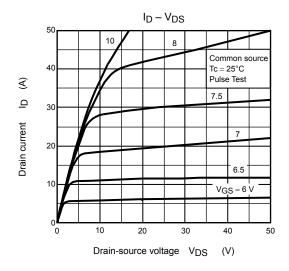
### Marking

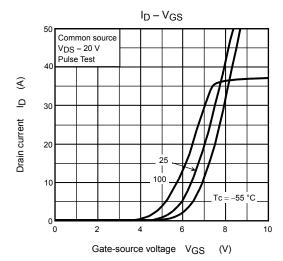


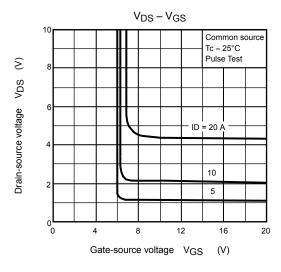
Note 4: A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

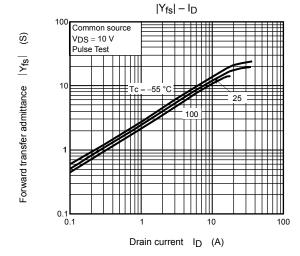
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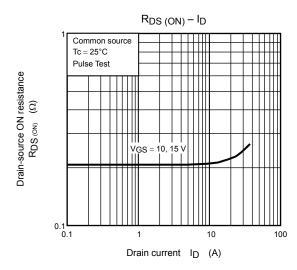




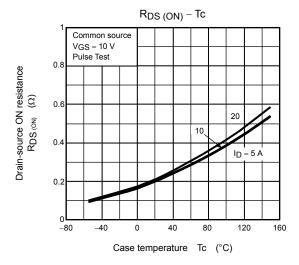


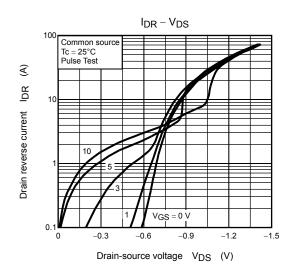


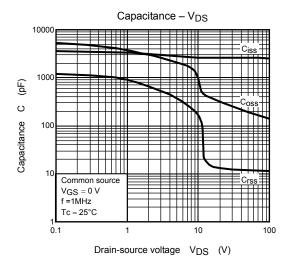


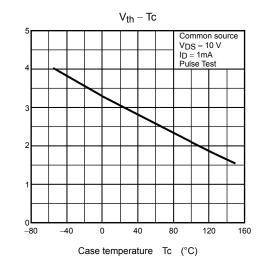


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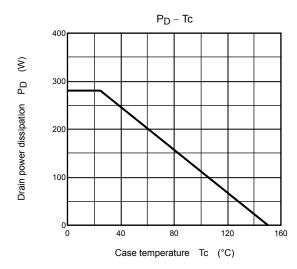


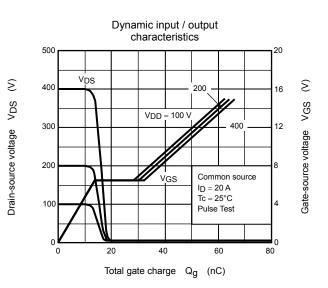
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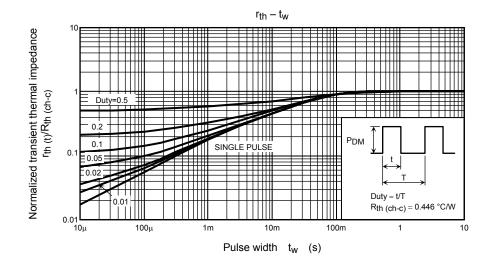
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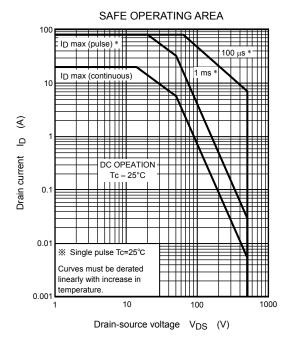
Gate threshold voltage

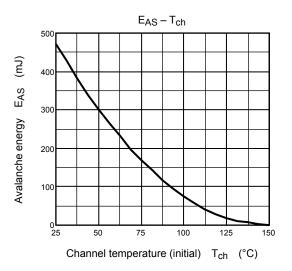
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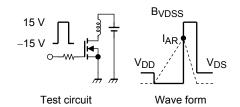












$$R_G = 25 \Omega$$
  
 $V_{DD} = 90 \text{ V}, L = 2.0 \text{ mH}$ 

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

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