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TOSHIBA Photocoupler GaAs Ired & Photo-Triac

TLP560G

Triac Driver Programmable Controllers AC-Output Module Solid State Relay

The TOSHIBA TLP560G consists of a photo-triac optically coupled to a gallium arsenide infrared emitting diode in a six lead plastic DIP package.

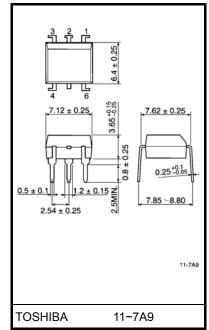
- Peak off-state voltage: 400V(min.)
- On-state current: 100mA(max.)
- Isolation voltage: 2500V_{rms}(min.)
- UL recognized: File No. E67349
- Isolation operating voltage: $2500V_{ac} \mbox{ or } 300V_{dc} \mbox{ for isolation} \label{eq:criterion}$ group $C^{\star 1}$
- Trigger LED current

Classi– fication*	Trigger LED V _T = 6V, ⁻	Marking of Classification	
	Min.	Max.	Classification
(IFT5)	_	5	T5
(IFT7)	_	7	T5, T7
Standard	_	10	T5, T7, blank

*Ex. (IFT5); TLP560G(IFT5)

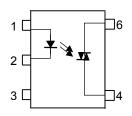
(Note) Application type name for certification test, please use standard product type name, i.e. TLP560G(IFT5): TLP560G

*1: According to VDE0110, table 4.



Weight: 0.39g (typ.)

Pin Configuration (top view)



1 : Anode

- 2 : Cathode
- 3 : N.C.

4 : Terminal 1 6 : Terminal 2 Unit in mm

Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit	
	Forward current	١ _F	50	mA		
	Forward current derating (Ta ≥	ΔI _F / °C	-0.7	mA / °C		
LED	Peak forward current (100µs pu	I _{FP}	1	А		
	Reverse voltage	V _R	5	V		
	Junction temperature	Tj	125	°C		
	Off-state output terminal voltage		V _{DRM}	400	V	
	On-state RMS current	Ta = 25°C		100	mA	
Detector		Ta = 70°C	I _{T(RMS)}	50	IIIA	
	On–state current derating (Ta ≥	ΔI _T / °C	-1.1	mA / °C		
	Peak on-state current (100µs p	I _{TP}	2	A		
	Peak nonrepetitive surge curren (Pw = 10ms, DC = 10%)	I _{TSM}	1.2	А		
	Junction temperature	Tj	115	°C		
Storage temperature range			T _{stg}	-55~125	°C	
Operating temperature range			T _{opr}	-40~100	°C	
Lead soldering temperature (10s)			T _{sol}	260	°C	
Isolation voltage (AC, 1min., R.H. ≤ 60%)			BVS	3V _S 2500		

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Recommended Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V _{AC}		—	120	Vac
Forward current	١ _F	15	20	25	mA
Peak on-state current	I _{TP}		_	1	А
Operating temperature	T _{opr}	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

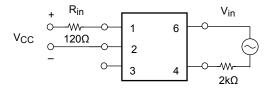
Individual Electrical Characteristics (Ta = 25°C)

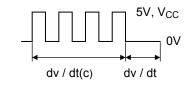
Characteristic		Symbol	Test Condition	Min.	Тур.	Max.	Unit
	Forward voltage	VF	I _F = 10mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5V	_	_	10	μA
	Capacitance	CT	V = 0, f = 1MHz	_	10	_	pF
Detector	Peak off-state current	IDRM	V _{DRM} = 400V	—	10	100	nA
	Peak on-state voltage	V _{TM}	I _{TM} = 100 mA	—	1.7	3.0	V
	Holding current	Iн	—	—	0.6	_	mA
	Critical rate of rise of off-state voltage	dv / dt	V _{in} = 120V _{rms} , Ta = 85°C (Fig. ²) 200	500	-	V / µs
	Critical rate of rise of commutating voltage	dv / dt(c)	V _{in} = 30V _{rms} , I _T = 15mA (Fig. ⁻) —	0.2	—	V / µs

Coupled Electrical Characteristics (Ta = 25°C)

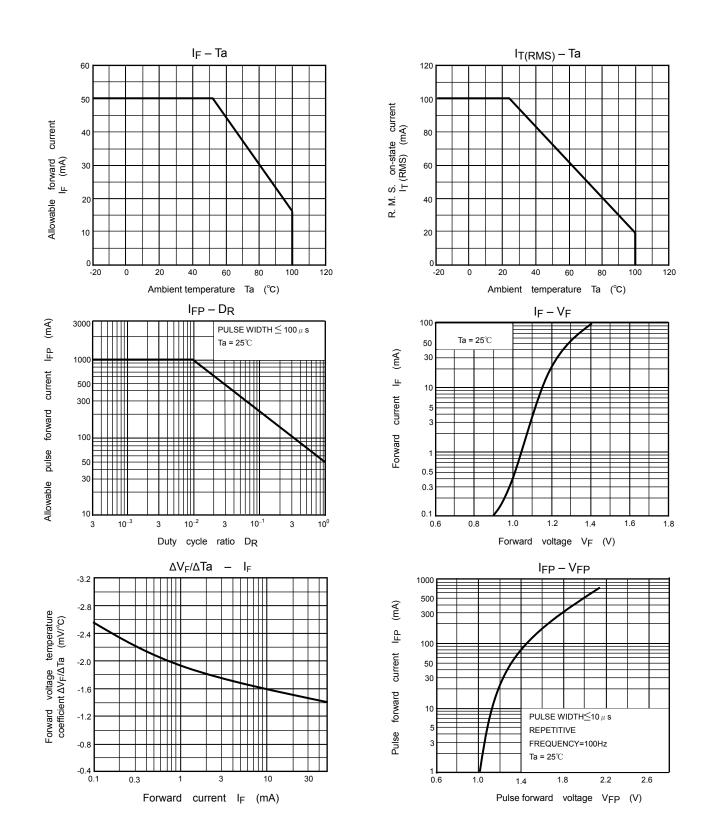
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Trigger LED current	I _{FT}	V _T = 3V	_	5	10	mA
Capacitance (input to output)	CS	V _S = 0, f = 1MHz		0.8		pF
Isolation resistance	R _S	V _S = 500V	5×10 ¹⁰	10 ¹⁴	_	Ω
	BVS	AC, 1 minute	2500	_	_	V
Isolation voltage		AC, 1 second, in oil	_	5000	_	V _{rms}
		DC, 1 minute, in oil	-	5000	_	V _{dc}

Fig.1: dv / dt test circuit

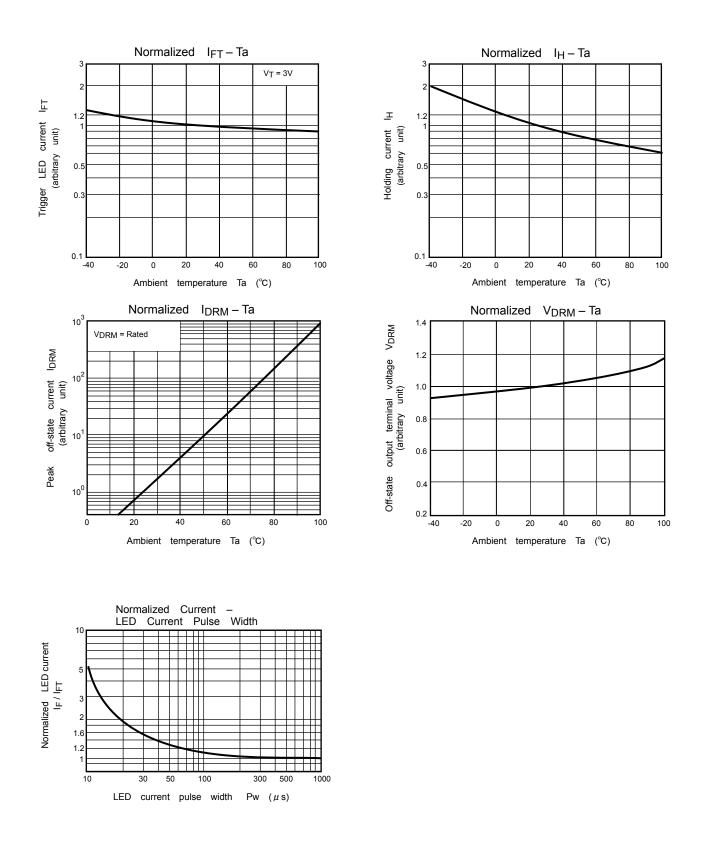




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